

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10002217
	Filing Date		2001-11-01
	First Named Inventor	Craig Nemecek	
	Art Unit	2123	
	Examiner Name	Jason Scott Proctor	
	Attorney Docket Number	CD01207M	

U.S.PATENTS

Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	5968135	B1	1999-10-19	Teramoto, et al.	Entire Document
	2	5399922	B1	1995-03-21	Kiani et al.	Entire Document
	3	5680070	B1	1997-10-21	Anderson et al.	Entire Document
	4	6144327	B1	2000-11-07	Distinti et al.	Entire Document
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	6	6166367	B1	2000-12-26	Cho	Entire Document
	7	5600262	B1	1997-02-04	Kolze	Entire Document
	8	5414308	B1	1995-05-09	Lee et al.	Entire Document

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	9	5258760	B1	1993-11-02	Moody et al.	Entire Document
	10	5563526	B1	1996-10-08	Hastings et al.	Entire Document
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	20	6157270	B1	2000-12-05	Tso	Entire Document
	21	4138671	B1	1997-02-06	Comer et al.	Entire Document
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	8	20020156929	A1	2002-10-24	Hekmatpour, Amir	Entire Document
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	1	9532478	WO	A1	1995-11-30	Klien	Entire Document	<input type="checkbox"/>
	2	0450863A2	EP	A1	1991-10-09	Austin	Entire Document	<input type="checkbox"/>
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	5	0639816A2	EP	A1	1995-02-22	McCollum	Entire Document	<input type="checkbox"/>
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	1	From Application Number 10/033,027 (CD00232): Goodenough, F. "Analog Counterparts of FPGAS Ease System Design" Electronic Design, Penton Publishing, Cleveland, OH, US vol. 42, no. 21, 10/14/94; 10 pages	<input type="checkbox"/>
	2	From Application Number 10/033,027 (CD00232): Harbaum, T. et al. "Design of a Flexible Coprocessor Unit" Proceedings of the Euromicro Conference, XX XX, Sept. 1999; 10 pages	<input type="checkbox"/>
	3	From Application Number 10/033,027; CD00169: "Programmable Microcontroller (PSoC) Architecture (Mixed Analog/Digital)"; 08/07/01; 09/924,734 Snyder et al.; 28 pages	<input type="checkbox"/>
	4	From Application Number 10/033,027; CD00170: "Digital Configurable Macro Architecture"; 07/18/01; 09/909,045; Snyder; 37 pages	<input type="checkbox"/>
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	7	From Application Number 10/033,027; CD00174; "Programmable Methodology and Architecture for a Programmable Analog System (As Amended)"; 08/14/01; 09/930,021; Mar et al.; 87 pages	<input type="checkbox"/>
	8	From Application Number 10/033,027; CD00175; "Method for Synchronizing and Resetting Clock Signals Supplied to Multiple Programmable Analog Blocks (As Amended)"; 10/01/01; 09/969,311; Sullam; 57 pages	<input type="checkbox"/>

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9	From Application Number 10/033,027: CD00180; "Method and Apparatus for Programming a Flash Memory"; 06/05/01; 09/875,599; Snyder; 23 pages	<input type="checkbox"/>
10	From Application Number 10/033,027: CD00182; "In-System Chip Emulator Architecture"; 10/10/01; 09/975,115; Snyder et al.; 38 pages	<input type="checkbox"/>
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12	From Application Number 10/033,027: CD00199; "Multiple Use of Microcontroller Pad"; 06/26/01; 09/893,050; Kutz et al.; 21 pages	<input type="checkbox"/>
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14	From Application Number 10/033,027: CD00227; "Architecture for Synchronizing and Resetting Clock Signals Supplied to Multiple Analog Programmable Analog Blocks"; 10/01/01; 09/969,313; Sullam; 50 pages	<input type="checkbox"/>
15	USPTO Non-Final Rejection for 09/994,601 (CD01170M) dated 11/14/2006; 15 pages	<input type="checkbox"/>
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20	GANAPATHY, GOPI, AND NARAYAN, RAM, AND JORDEN, GLEN, AND FERNANDEZ, DENZIL, AND WANG, MING, AND NISHIMURA, JIM; "Hardware Emulation for Functional Verification of K5", June 1996, 33rd Design Automation Conference Proceedings, 3-7 June 1996; 4 pages	<input type="checkbox"/>
21	Five unnumbered pages from the Provisional Application 60/243,708; 5 pages	<input type="checkbox"/>
22	"Webster's Third New International Dictionary", 1996. G. & C. Merriam Company; 3 pages	<input type="checkbox"/>
23	USPTO Advisory Action for 09/998,848 (CD01177) dated 09/07/05; 3 pages	<input type="checkbox"/>
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31	USPTO Non-Final Rejection for 10/008,096, dated 04/17/2006; 18 pages	<input type="checkbox"/>
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